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Agreed  
11/1/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/602,395  
Filing Date ..... June 22, 2000  
Inventor ..... Moore  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Nguyen, T.  
Attorney's Docket No. .... MI22-1384  
Title: Methods of Forming Oxide Regions Over Semiconductor Substrates

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: October 31, 2001

By: Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711

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